

**Amendments to the Abstract:**

Please replace the ABSTRACT with the following rewritten ABSTRACT:

--A disclosed PLL can operate linearly at various frequencies by overlapping several VCOs and using a control circuit for controlling the several VCOs to select a VCO at a desired frequency. Accordingly, ~~since the disclosed~~ PLL can automatically pre-compensate the frequency by using a control signal used in the PLL[.]. As a result, a separate frequency ~~correcting compensation~~ signal is not required. ~~When containing a VCO, the The~~ disclosed PLL can be configured [[into]] on a single chip[.] when the VCO is contained within the PLL.--